VIDYASAGAR UNIVERSITY Midnapore, WB, PIN-721102 PROVISIONAL MERIT LIST FOR 20% SEATS (2024-2025) FOR PG PROGRAMME M.SC. IN ELECTRONICS OBC-A - PWD CATEGORY LIST

*) Seats will be filled subject to availablity of seats under different category maintaining the eligibility criteria.

*) This merit list has been generated on the basis of marks obtained in the Common Entrance Test along with unverified data submitted by the candidates. If found, any falsification of data, the admission will be instantly cancelled at any stage of the study.

List prepared with data as on : 24-09-2024 06:48

Sl No.	Form No.	Roll No.	Name	CET Obtn.	% in BLIS	% in PG	% in Hons	% in 10+2	% in 10	Univ	Social Cat	DA %	EWS?
No data Available													
*** EOF ***													

(FSAC : Final Semester Appearing Candidate in Qualifying Degree , NA : Not Applicable)

List Generated on: 2024-09-24 18:48