

# VIDYASAGAR UNIVERSITY

Midnapore, WB, PIN-721102

PROVISIONAL MERIT LIST OF 'WILLINGNESS OF ADMISSION' FOR 80% SEATS (2024-2025)

**FOR PG/BLISC PROGRAMME**

**M.SC. IN ELECTRONICS**

**OBC-B - PWD CATEGORY LIST**

\*) Seats will be filled subject to availability of seats under different category maintaining the eligibility criteria.

\*) This merit list has been generated on the basis of willingness of admission along with marks obtained in the Common Entrance Test and unverified data submitted by the candidates. If found, any falsification of data, the admission will be instantly cancelled at any stage of the study.

List prepared with data as on : 12-11-2024 05:07

Sl No.	Form No.	Roll No.	Name	CET Obtn.	% in BLIS	% in PG	% in Hons	% in 10+2	% in 10	Univ	Social Cat	DA %	EWS?
<b>No data Available</b>													
<b>*** EOF ***</b>													

# (FSAC : Final Semester Appearing Candidate in Qualifying Degree , NA : Not Applicable)

# List Generated on: 2024-11-12 17:07